

**Notice of References Cited**

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Applicant(s)/Patent Under

Reexamination

HELLBERG, TINO

Examiner

Khanh Dang

Art Unit

2111

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,210,846	05-1993	Lee, Robert D.	710/115
*	B	US-5,708,799	01-1998	Gafken et al.	710/301
*	C	US-6,532,506	03-2003	Dunstan et al.	710/100
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Overview of 1-Wire Technology and Its Use, Maxim, Dallas Semiconductor, 12/2002
	V	Creating Non-Linear Transfer Functions With Linear Potentiometer Circuits, Maxim, Dalls Semiconductor, 11/2001
	W	Definition of Voltage Divider Rule by Wikipedia
	X	Definition of 1-Wire Bus by Wikipedia

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.